

**Vishay Semiconductors** 

## Standard Recovery Diodes (Stud Version), 12 A



PRODUCT SUMMARY	
E(A)A	12 A

### FEATURES

- High surge current capability
- Stud cathode and stud anode version
- Wide current range
- Types up to 1200 V V<sub>RRM</sub>
- Designed and qualified for industrial and consumer level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

- Battery charges
- Converters
- Power supplies
- Machine tool controls

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I <sub>F(AV)</sub>		12	А	
	T <sub>C</sub>	144	°C	
I <sub>F(RMS)</sub>		19	А	
I <sub>FSM</sub>	50 Hz	265	٨	
	60 Hz	280	A	
l²t	50 Hz	351	A <sup>2</sup> s	
	60 Hz	320	A-5	
V <sub>RRM</sub>	Range	100 to 1200	V	
TJ		- 65 to 175	°C	

### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS					
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK VOLTAGE V	V <sub>R(BR)</sub> , MINIMUM AVALANCHE VOLTAGE V <sup>(1)</sup>	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = 175 °C mA
	10	100	150	-	
	20	200	275	-	
	40	400	500	500	
12F(R)	60	600	725	750	12
	80	800	950	950	
	100	1000	1200	1150	
	120	1200	1400	1350	

Note

 $^{(1)}\,$  Avalanche version only available from  $V_{\text{RRM}}\,400$  V to 1200 V

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FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	I <sub>F(AV)</sub>	180° conduction, half sine wave		12	A	
at case temperature	. ( )		-		144	°C
Maximum RMS forward current	I <sub>F(RMS)</sub>				19	A
Maximum on-repetitive peak reverse power	P <sub>R</sub> <sup>(1)</sup>	10 µs squar	e pulse, $T_J = T_J m$	naximum	7	K/W
		t = 10 ms	No voltage		265	A
Maximum peak, one-cycle forward, non-repetitive surge current	I <sub>FSM</sub>	t = 8.3 ms	reapplied	Sinusoidal half wave, initial T <sub>J</sub> = T <sub>J</sub> maximum	280	
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		225	
		t = 8.3 ms			235	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	t = 10 ms	No voltage		351	A <sup>2</sup> s
		t = 8.3 ms	reapplied		320	
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		250	
		t = 8.3 ms			226	
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied		3510	A²√s	
Low level value of threshold voltage	V <sub>F(TO)1</sub>	(16.7 % x $\pi$ x I <sub>F(AV)</sub> < I < $\pi$ x I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum		0.77	v	
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > \pi x I_{F(AV)}), T_J = T_J maximum$		0.97	v	
Low level value of forward slope resistance	r <sub>f1</sub>	(16.7 % x $\pi$ x I <sub>F(AV)</sub> < I < $\pi$ x I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum		10.70		
High level value of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J maximum$			6.20	mΩ
Maximum forward voltage drop	V <sub>FM</sub>	$I_{pk}$ = 38 A, $T_J$ = 25 °C, $t_p$ = 400 µs rectangular wave			1.26	V

#### Note

<sup>(1)</sup> Available only for avalanche version, all other parameters the same as 12F

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating temperature range	TJ		- 65 to 175	°C	
Maximum storage temperature range	T <sub>Stg</sub>		- 65 to 200		
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	2	K/W	
Maximum thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth, flat and greased		rv VV	
		Not lubricated threads	1.5 + 0 - 10 %	N·m	
Allowable mounting torque			13	lbf ∙ in	
Allowable mounting torque			1.2 + 0 - 10 %	N·m	
		Lubricated threads	10	lbf ∙ in	
Approximate weight			7	g	
Approximate weight			0.25	oz.	
Case style		See dimensions - link at the end of datasheet	DO-203AA	A (DO-4)	

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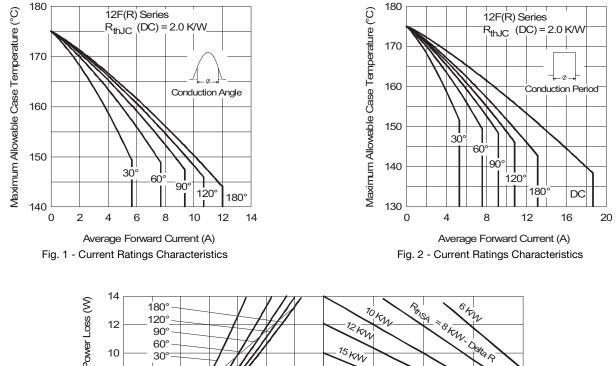


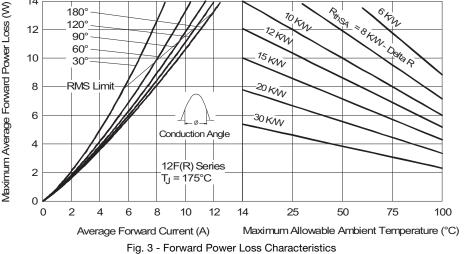
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CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.33	0.26			
120°	0.41	0.44			
90°	0.53	0.58	$T_J = T_J$ maximum	K/W	
60°	0.78	0.81			
30°	1.28	1.29			

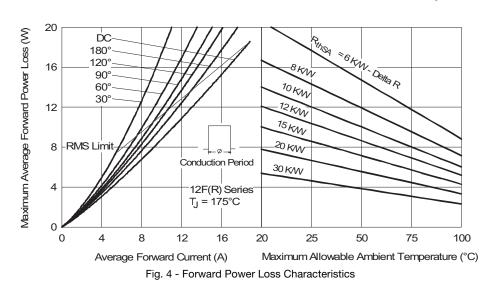
#### Note

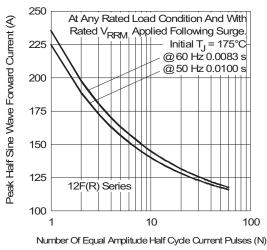
The table above shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC





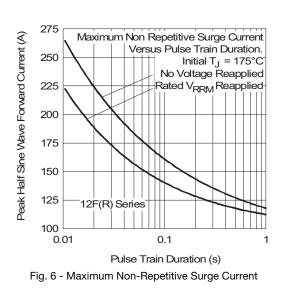
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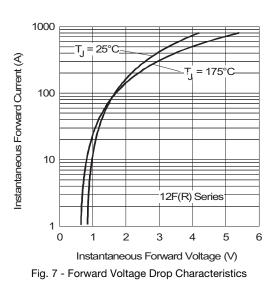




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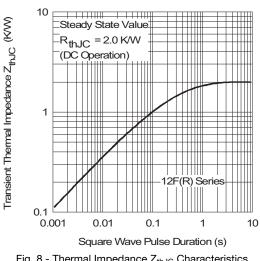


Fig. 8 - Thermal Impedance  $Z_{\text{thJC}}$  Characteristics

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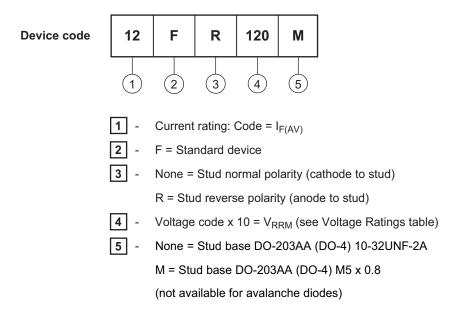
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### **Vishay Semiconductors**

### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95311			



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R 0.40 R (0.02)

Ø 6.8 (0.27)

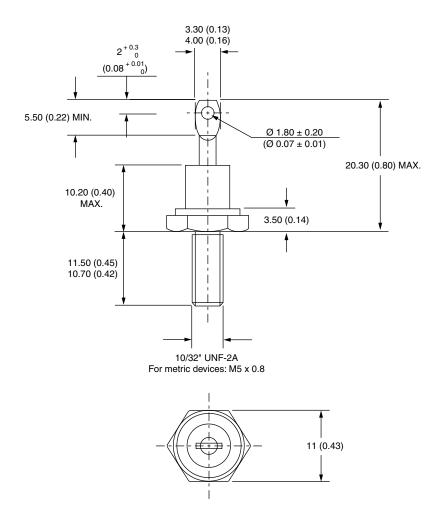
 $0.8 \pm 0.1$ 

 $(0.03 \pm 0.004)$ 



# DO-203AA (DO-4)

### **DIMENSIONS** in millimeters (inches)







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